

RELEASE Ø Data Sheet



GRF2072 Ultra-Low Noise Amplifier 1.5 to 3.8 GHz

FEATURES

- Excellent NF Performance
- High Gain
- Outstanding Linearity
- Flexible Bias Voltage and Current
- Compact 2.0 x 2.0 mm DFN-8 Package
- Process: GaAs pHEMT

Reference: 5 V / 2.5 GHz / 70 mA

- Gain: 19.8 dB
- OIP3: 37.5 dBm
- OP1dB: 20 dBm
- Evaluation Board Noise Figure: 0.55 dB

APPLICATIONS

- Cellular Infrastructure
- Small Cells and Cellular Repeaters
- Distributed Antenna Systems

DESCRIPTION

The GRF2072 is a broadband, linear, ultra-low noise amplifier designed for small cell, wireless infrastructure, and other high performance RF applications requiring ultra-low NF, high Gain, and linearity.

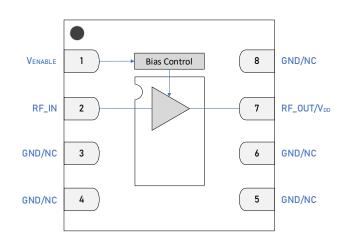
This device is a member of a family of pin-compatible, ultra-low noise devices which cover a wide range of frequency bands with industry leading NF and Gain:

GRF2070: 0.1 to 1.5 GHz	GRF2071: 0.7 to 2.7 GHz
GRF2072: 1.5 to 3.8 GHz	GRF2073: 2.0 to 6.0 GHz

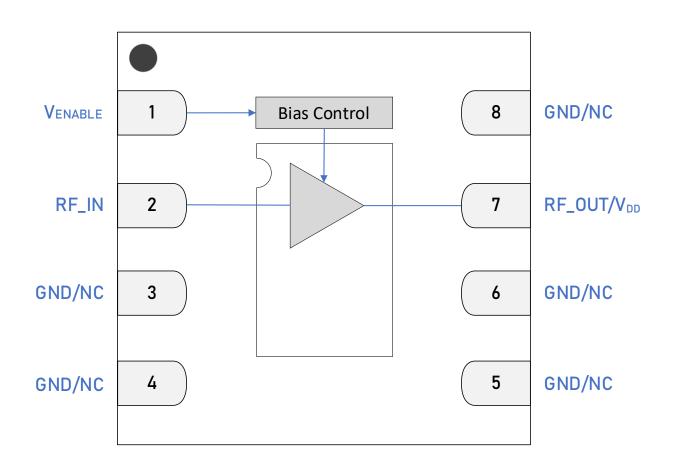
GRF2074: 1.0 to 6.0 GHz (Next-gen Process)

Please consult with the GRF applications engineering team for application notes and custom tuning/evaluation board data. De-embedded S-Parameters with Noise Parameters are available on the website.

BLOCK DIAGRAM







2.0 x 2.0 mm DFN-8 Pin Out (Top View)



Pin Assignments

Pin	Name	Description	Note
1	Venable	Enable Voltage Input	V_{ENABLE} and series resistor set I_{DDQ} . $V_{\text{ENABLE}} \leq 0.2$ volts disables device.
2	RF_IN	RF Input	External match must provide DC block.
3, 4, 5, 6, 8	GND/NC	No Connect or Ground	No internal connection to die. We recommend connecting these pins to GND.
7	RF_Out/V _{DD}	RF Output	Provide device V _{DD} via external bias inductor.
PKG BASE	GND	Ground	Provides DC and RF ground for LNA, as well as thermal heat sink. Recommend multiple 8 mil vias beneath the package for optimal RF and thermal performance. Refer to evaluation board top layer graphic on schematic page.



Absolute Ratings

Parameter	Symbol	Min.	Max.	Unit
Supply Voltage	V _{DD}	0	6	V
RF Input Power (CW, Load VSWR < 2:1, V_{DD} = 5 V)	P _{IN MAX}		23	dBm
Operating Temperature (Package Heat Sink)	Tpkg heat sink	-40	105	°C
Maximum Channel Temperature (MTTF >10 ⁶ Hours)	Тмах		170	°C
Maximum Dissipated Power	Pdiss max		500	mW
Electrostatic Discharge	1	1	1	1
Charged Device Model	CDM	1500		V
Human Body Model	HBM	500		V
Storage				
Storage Temperature	T _{STG}	-65	150	°C
Moisture Sensitivity Level	MSL		1	



Caution! ESD Sensitive Device.

Exceeding Absolute Maximum Rating conditions may cause permanent damage.

Note: For additional information, please refer to *Manufacturing Note MN-001 — Package and Manufacturing Information*.



All Guerrilla RF products are provided in RoHS compliant lead (Pb)-free packaging requiring no exemptions. Additional information for this topic can be found at this link - *Environmental and Restricted Substance Statement Library*



Recommended Operating Conditions

		Specification				
Parameter	Symbol	Min.	Тур.	Max.	Unit	Condition
Supply Voltage	V _{DD}	0	5	6	V	
Operating Temperature (Package Heat Sink)	Tpkg heat sink	-40		105	°C	
RF Frequency Range	F _{RF}	1.5	2.5	3.8	GHz	Typical Application Schematic with external matching components (note 1 & 2).

Note 1: Operation outside this range is possible, but with degraded performance of some parameters.

Note 2: Contact the Guerrilla RF Applications team for guidance on optimizing the tuning of the device for alternative bands.



Nominal Operating Parameters – General

The following conditions apply unless noted otherwise: Typical Application Schematic using the 1.5 to 3.8 GHz tuning set, M3 = 900 Ω , V_{DD} = 5 V, V_{ENABLE} = 5 V, I_{DD} = 70 mA, F_{TEST} = 2.5 GHz, T_{PKG HEAT SINK} = 25 °C. Evaluation board losses are included within the specifications.

		Specification				
Parameter	Symbol	Min.	Тур.	Max.	Unit	Condition
Supply Current	I _{DD}		70		mA	Adjustable for optimal IP3.
Enable Current	IENABLE		3.5		mA	
Switching Rise Time	T _{RISE}		500		ns	
Switching Fall Time	TFALL		500		ns	

Thermal Data

Thermal Resistance: (Infrared Scan)	ΟJC		60		°C	On standard evaluation board (note 3).
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Note 3: MTTF >10^6 hours for $T_{CHANNEL} \le 170$ °C.



Nominal Operating Parameters – RF

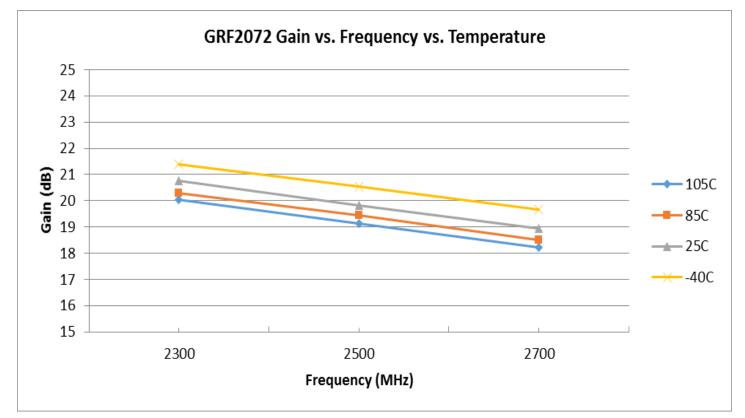
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			Specification				
Parameter	Symbol	Min.	Тур.	Max.	Unit	Condition	
Gain	S21	18.8	19.8		dB		
Reverse Isolation	S12		-26		dB	F _{RF} : 2.5 to 2.7 GHz	
Evaluation Board Noise Figure	NF		0.55	0.75	dB	Evaluation Board SMA to SMA	
Output 3 rd Order Intercept	OIP3		37.5		dBm	+3 dBm P _{out} per tone at 2 MHz Spacing (2499 and 2501 MHz)	
Output 1 dB Compression Power	OP1dB	18.5	20		dBm		

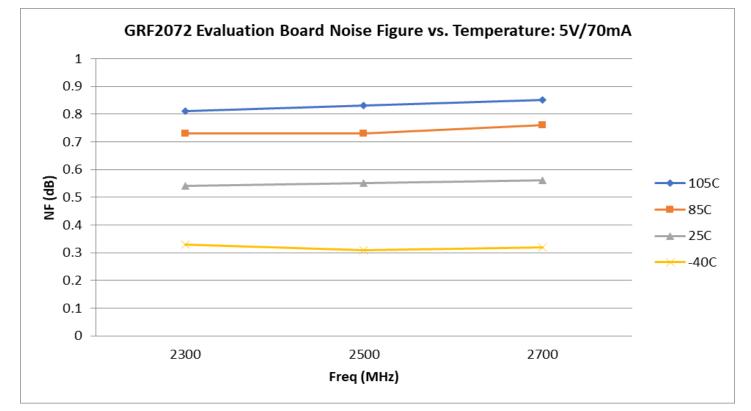
Typical Operating Curve Conditions

The following conditions apply unless noted otherwise: Typical Application Schematic using the 1.5 to 3.8 GHz tuning set, M3 = 900 Ω , V_{DD} = 5 V, V_{ENABLE} = 5 V, I_{DD} = 70 mA, F_{TEST} = 2.5 GHz, T_{PKG HEAT SINK} = 25 °C. Evaluation board losses are included within the plots.



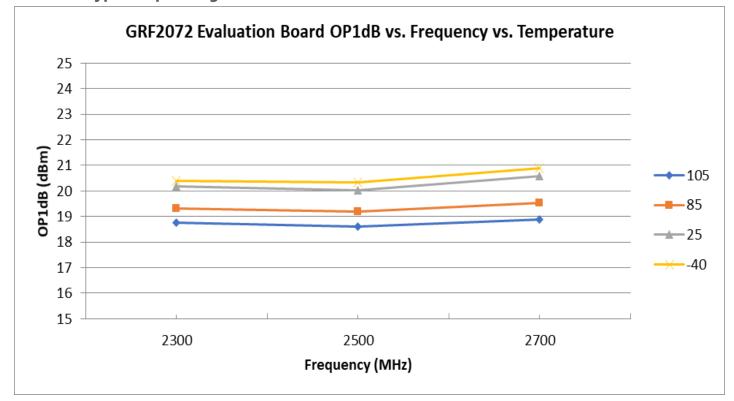


GRF2072 Typical Operating Curves

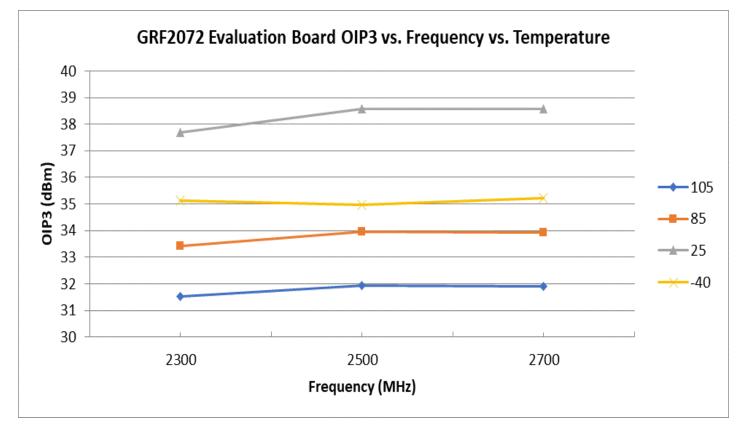


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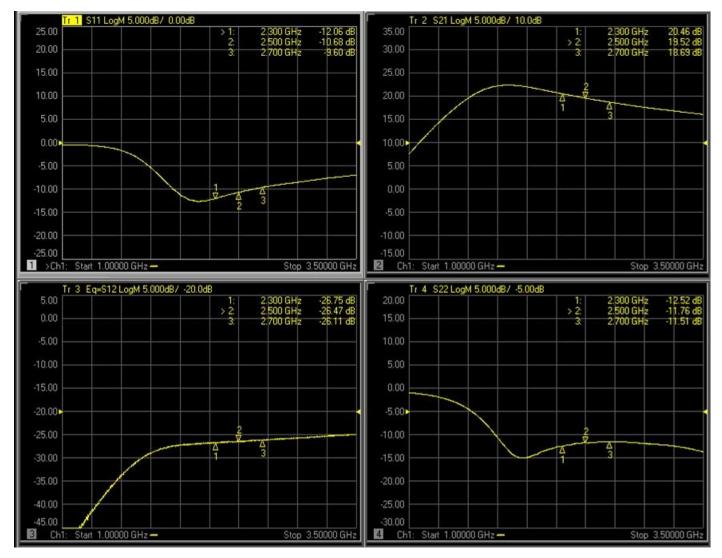




GRF2072 Typical Operating Curves

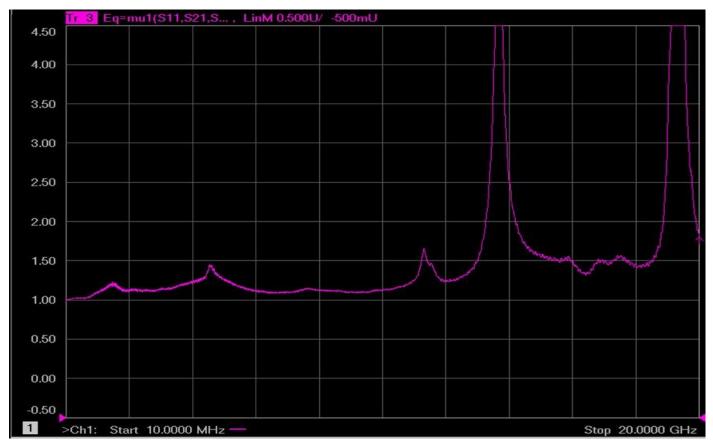






GRF2072 Typical Operating Curves: S-Parameters (2.3 to 2.7 GHz Tune)





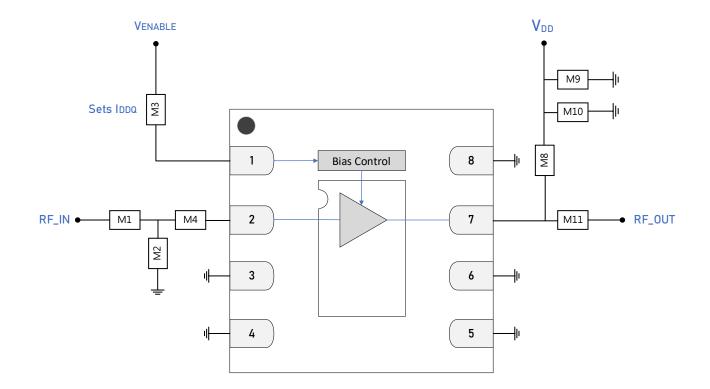
GRF2072 Typical Operating Curves: Stability Mu Factor (2.3 to 2.7 GHz Tune)

Note: Mu Factor ≥ 1.0 implies unconditional stability.

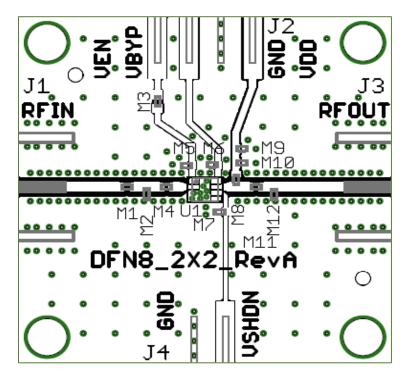


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GRF2072 Standard Test Schematic



GRF2072 Evaluation Board Assembly Diagram

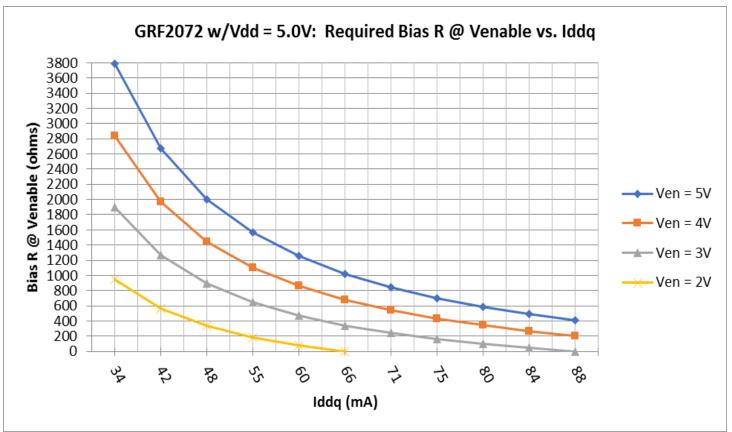
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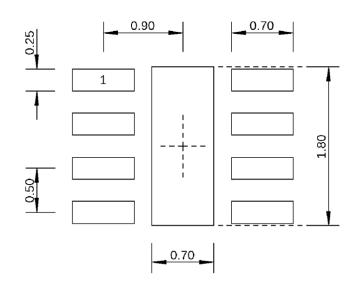
Component	Туре	Manufacturer	Family	Value	Package Size	Substitution
M1	Capacitor	Murata	GJM	20 pF	0402	ok
M2	Inductor	Murata	LQG	2.7 nH	0402	ok
M3	Resistor (See Curves)	Various	5%	Sets I _{DDQ}	0402	ok
M4	Capacitor	Murata	GRM	2.2 pF	0402	ok
M8	Inductor	Murata	LQG	2.7 nH	0402	ok
M9	Capacitor	Murata	GRM	0.1 μF	0402	ok
M10	Capacitor	Murata	GRM	100 pF	0402	ok
M11	Capacitor	Murata	GRM	2.0 pF	0402	ok
Evaluation Board	DFN8_2x2_RevA					

GRF2072 Evaluation Board Assembly Diagram Reference (2.3 to 2.7 GHz Tune)

GRF2072 Bias Resistor Selection Curves:

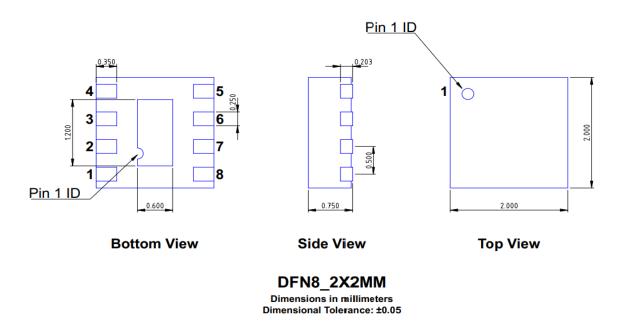






Dimensions in millimeters

2.0 x 2.0 mm DFN-8 Suggested PCB Footprint (Top View)



2.0 x 2.0 mm DFN-8 Package Dimensions



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Package Marking Diagram



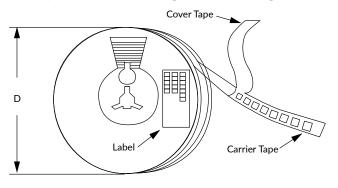
- Line 1: "Y" = YEAR (single digit). "WW" = WORK WEEK the Device was assembled.
- Line 2: "XXXX" = Device PART NUMBER.

Tape and Reel Information

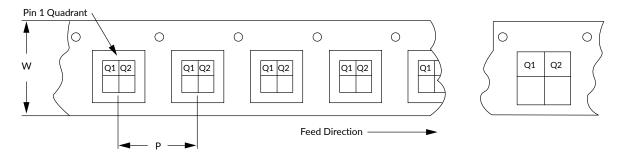
Guerrilla RF's tape and reel specification complies with Electronics Industries Association (EIA) standards for "Embossed Carrier Tape of Surface Mount Components for Automatic Handling" (reference EIA-481). See the following page for the Tape and Reel Specification and Device Package Information table, which includes units per reel.

Devices are loaded with pins down into the carrier pocket with protective cover tape and reeled onto a plastic reel. Each reel is packaged in a cardboard box. There are product labels on the reel, the protective ESD bag and the outside surface of the box.

For the Tape and Reel Reference Table, please refer to: Package Manufacturing Information | Guerrilla RF (guerrilla-rf.com)



Tape and Reel Packaging with Reel Diameter Noted (D)



Carrier Tape Width (W), Pitch (P), Feed Direction and Pin 1 Quadrant Information

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Revision History

Revision Date	Description of Change					
September 8, 2021	Converted format to new template.					
March 8, 2022	Updated Package Marking Diagram.					



Data Sheet Classifications

Data Sheet Status	Notes
Advance	S-parameter and NF data based on EM simulations for the fully packaged device using foundry-supplied transistor S-parameters. Linearity estimates based on device size, bias condition and experience with related devices.
Preliminary	All data based on evaluation board measurements taken within the Guerrilla RF Applications Lab. Any MIN/MAX limits represented within the data sheet are based solely on <i>estimated</i> part-to-part variations and process spreads. All parametric values are subject to change pending the collection of additional data.
Release Ø	All data based on measurements taken with <i>production-released</i> material. TYP values are based on a combination of ATE and bench-level measurements, with MIN/MAX limits defined using <i>modelled estimates</i> that account for part-to-part variations and expected process spreads. Although unlikely, future refinements to the TYP/MIN/MAX values may be in order as multiple lots are processed through the factory.
Release A-Z	All data based on measurements taken with production-released material <i>derived from multiple lots which have been fabricated over an extended period of time.</i> MIN/MAX limits may be refined over previous releases as more statistically significant data is collected to account for process spreads.

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