



GRF2071

Ultra-Low Noise Amplifier 0.7 to 2.7 GHz

FEATURES

- Excellent Noise Figure Performance
- High Gain
- Outstanding Linearity
- Flexible Bias Voltage and Current
- Compact 2.0 x 2.0 mm DFN-8 Package
- Process: GaAs pHEMT

Reference: 5 V / 70 mA / 1.9 GHz

- Gain: 19 dB
- OIP3: 36 dBm
- OP1dB: 20 dBm
- Evaluation Board Noise Figure: 0.36 dB

APPLICATIONS

- Cellular Infrastructure
- Small Cells and Cellular Repeaters
- Distributed Antenna Systems
- High Performance GPS

DESCRIPTION

The GRF2071 is a broadband, linear, ultra-low noise amplifier designed for small cell, wireless infrastructure, and other high performance RF applications requiring ultra-low NF, high gain and linearity.

This device is a member of a family of pin-compatible, ultra-low noise devices which cover a wide range of frequency bands with industry leading NF and gain:

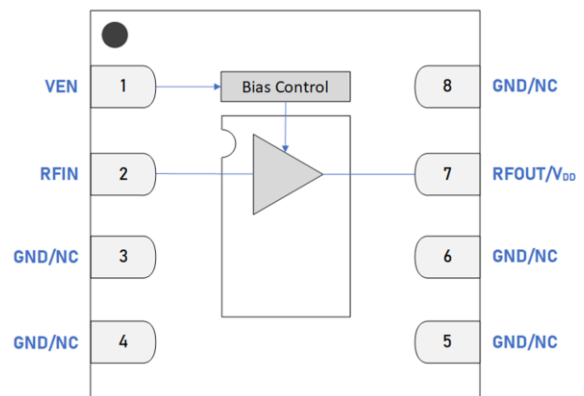
[GRF2070](#): 0.1 to 1.5 GHz [GRF2071](#): 0.7 to 2.7 GHz

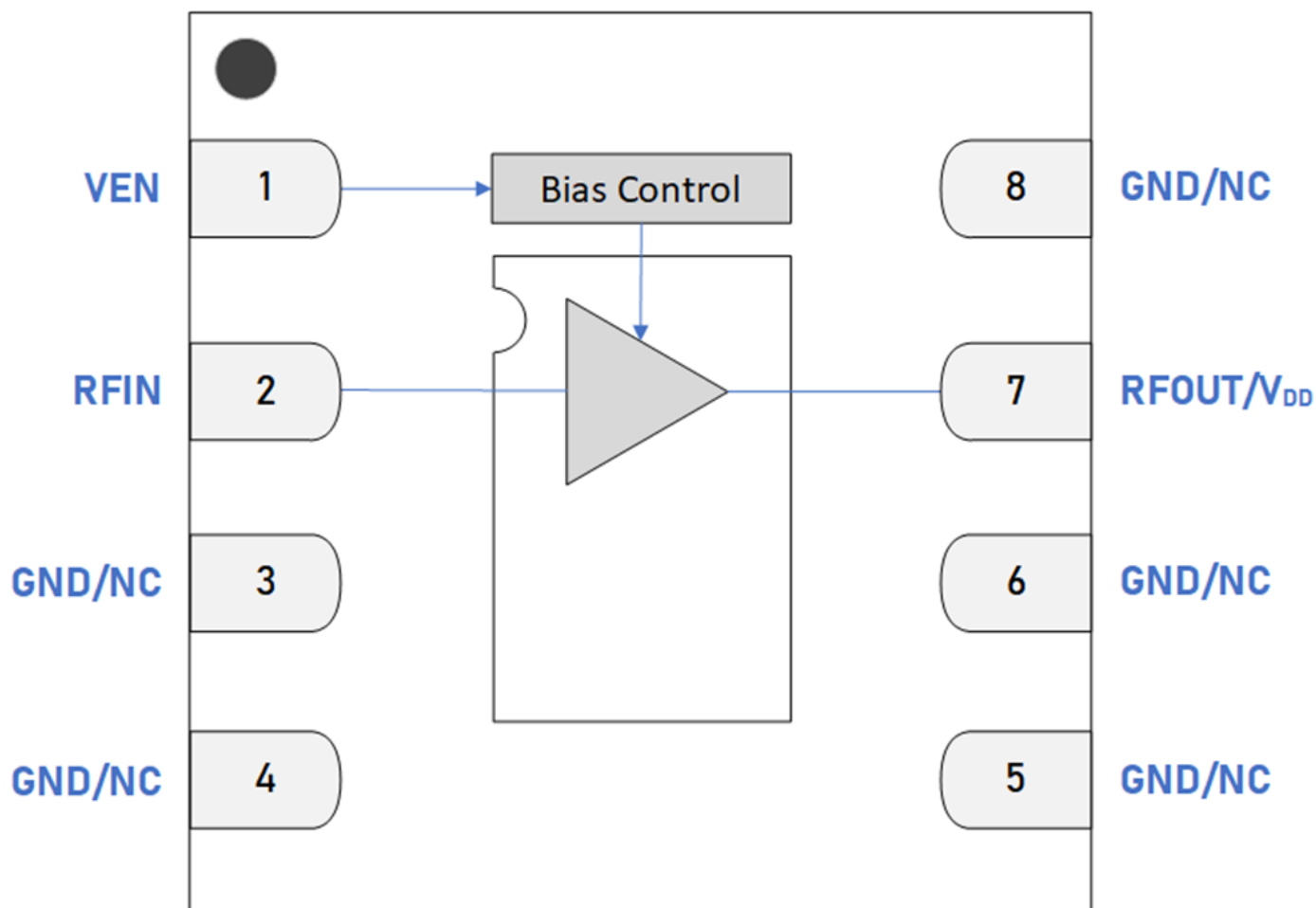
[GRF2072](#): 1.5 to 3.8 GHz [GRF2073](#): 2.0 to 6.0 GHz

[GRF2074](#): 1.0 to 6.0 GHz (Next-gen Process)

Please consult with the GRF applications engineering team for application notes and custom tuning/evaluation board data. De-embedded S-Parameters with Noise Parameters are available on the website.

BLOCK DIAGRAM





2.0 x 2.0 mm DFN-8 Pin Out (Top View)

Pin Assignments

Pin	Name	Description	Note
1	V _{ENABLE}	Enable Voltage Input	V _{ENABLE} and series resistor set I _{DDQ} . V _{ENABLE} ≤ 0.2 volts disables device.
2	RF_IN	RF Input	External match must provide DC block.
3, 4, 5, 6, 8	GND/NC	Ground or No Connect	No internal connection to die. We recommend connecting these pins to GND.
7	RF_Out/V _{DD}	RF Output	Provide device V _{DD} via external bias inductor.
PKG BASE	GND	Ground	Provides DC and RF ground for LNA, as well as thermal heat sink. Recommend multiple 8 mil vias beneath the package for optimal RF and thermal performance. Refer to evaluation board top layer graphic on schematic page.

Absolute Ratings

Parameter	Symbol	Min.	Max.	Unit
Supply Voltage	V_{DD}	0	6	V
RF Input Power (CW, Load VSWR < 2:1, V_{DD} = 5 V)	$P_{IN\ MAX}$		23	dBm
Operating Temperature (Package Heat Sink)	$T_{PKG\ HEAT\ SINK}$	-40	105	°C
Maximum Channel Temperature (MTTF > 10 ⁶ Hours)	T_{MAX}		170	°C
Maximum Dissipated Power	$P_{DISS\ MAX}$		500	mW

Electrostatic Discharge

Charged Device Model	CDM	1500		V
Human Body Model	HBM	500		V

Storage

Storage Temperature	T_{STG}	-65	150	°C
Moisture Sensitivity Level	MSL		1	--



Caution! ESD Sensitive Device.

Exceeding Absolute Maximum Rating conditions may cause permanent damage.

Note: For additional information, please refer to [Manufacturing Note MN-001 — Package and Manufacturing Information](#).



All Guerrilla RF products are provided in RoHS compliant lead (Pb)-free packaging requiring no exemptions. Additional information for this topic can be found at this link - [Environmental and Restricted Substance Statement Library](#)

Recommended Operating Conditions

Parameter	Symbol	Specification			Unit	Condition
		Min.	Typ.	Max.		
Supply Voltage	V_{DD}	0	5	6	V	
Operating Temperature (Package Heat Sink)	$T_{PKG \text{ HEAT SINK}}$	-40		105	°C	
RF Frequency Range	F_{RF}	0.7	1.9	2.7	GHz	Typical Application Schematic with external matching components (note 1 & 2).

Note 1: Operation outside this range is possible, but with degraded performance of some parameters.

Note 2: Contact the Guerrilla RF Applications team for guidance on optimizing the tuning of the device for alternative bands.

Nominal Operating Parameters – General

The following conditions apply unless noted otherwise: Typical Application Schematic using the 1.7 to 2.7 GHz tuning set, $M3 = 1070\ \Omega$, $V_{DD} = 5\text{ V}$, $V_{ENABLE} = 5\text{ V}$, $I_{DD} = 70\text{ mA}$, $F_{TEST} = 1.9\text{ GHz}$, $T_{PKG\ HEAT\ SINK} = 25\text{ }^{\circ}\text{C}$. Evaluation board losses are included within the specifications.

Parameter	Symbol	Specification			Unit	Condition
		Min.	Typ.	Max.		
Supply Current	I_{DD}	50	70	90	mA	
Enable Current	I_{ENABLE}		3		mA	
Switching Rise Time	T_{RISE}		700		ns	
Switching Fall Time	T_{FALL}		300		ns	

Disabled Mode

Leakage Current	$I_{LEAKAGE}$		150	250	μA	$V_{DD} = 5\text{ V}$, $V_{ENABLE} = 0\text{ V}$.
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Thermal Data

Thermal Resistance: (Infrared Scan)	Θ_{JC}		60		$^{\circ}\text{C}/\text{W}$	On standard evaluation board (note 3).
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Note 3: MTTF > 10^6 hours for $T_{CHANNEL} \leq 170\text{ }^{\circ}\text{C}$.

Nominal Operating Parameters – RF

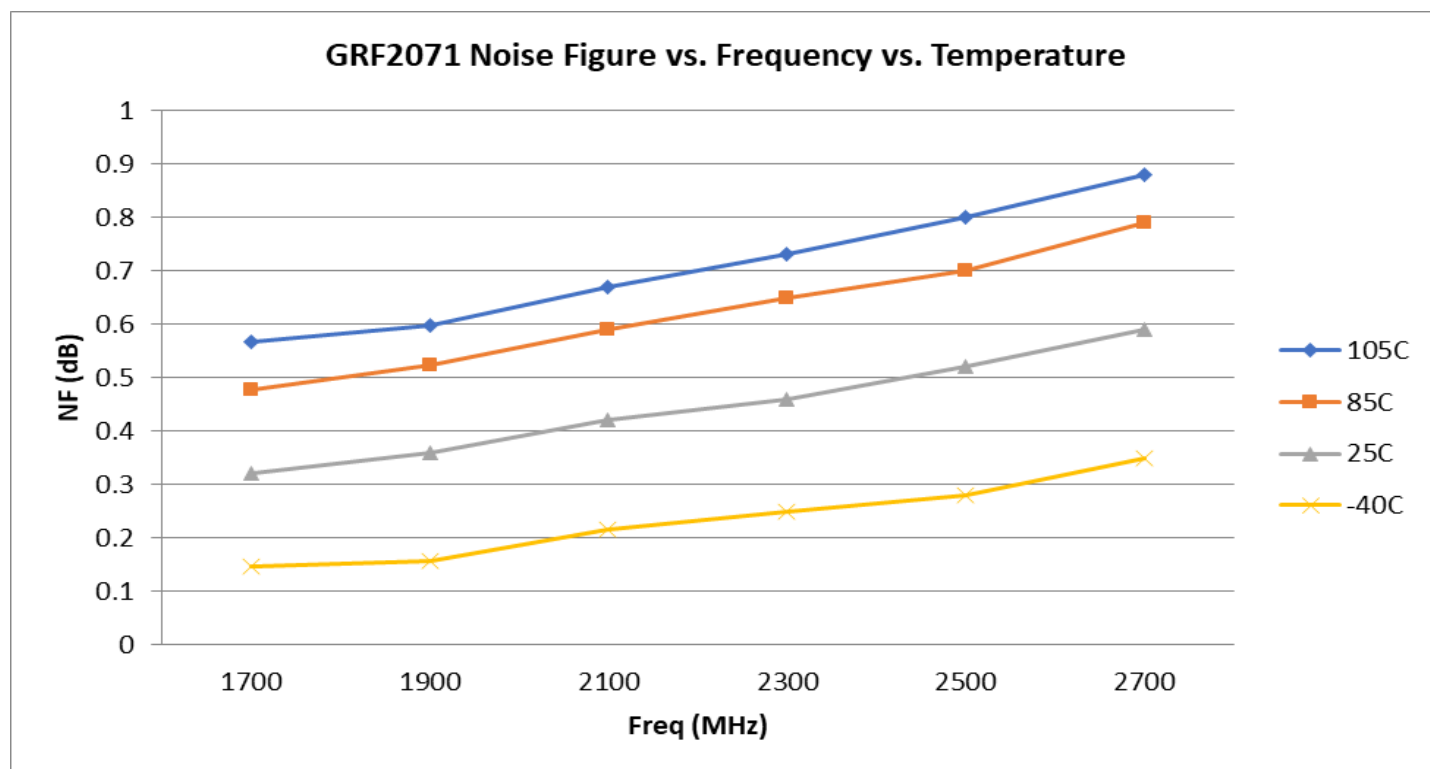
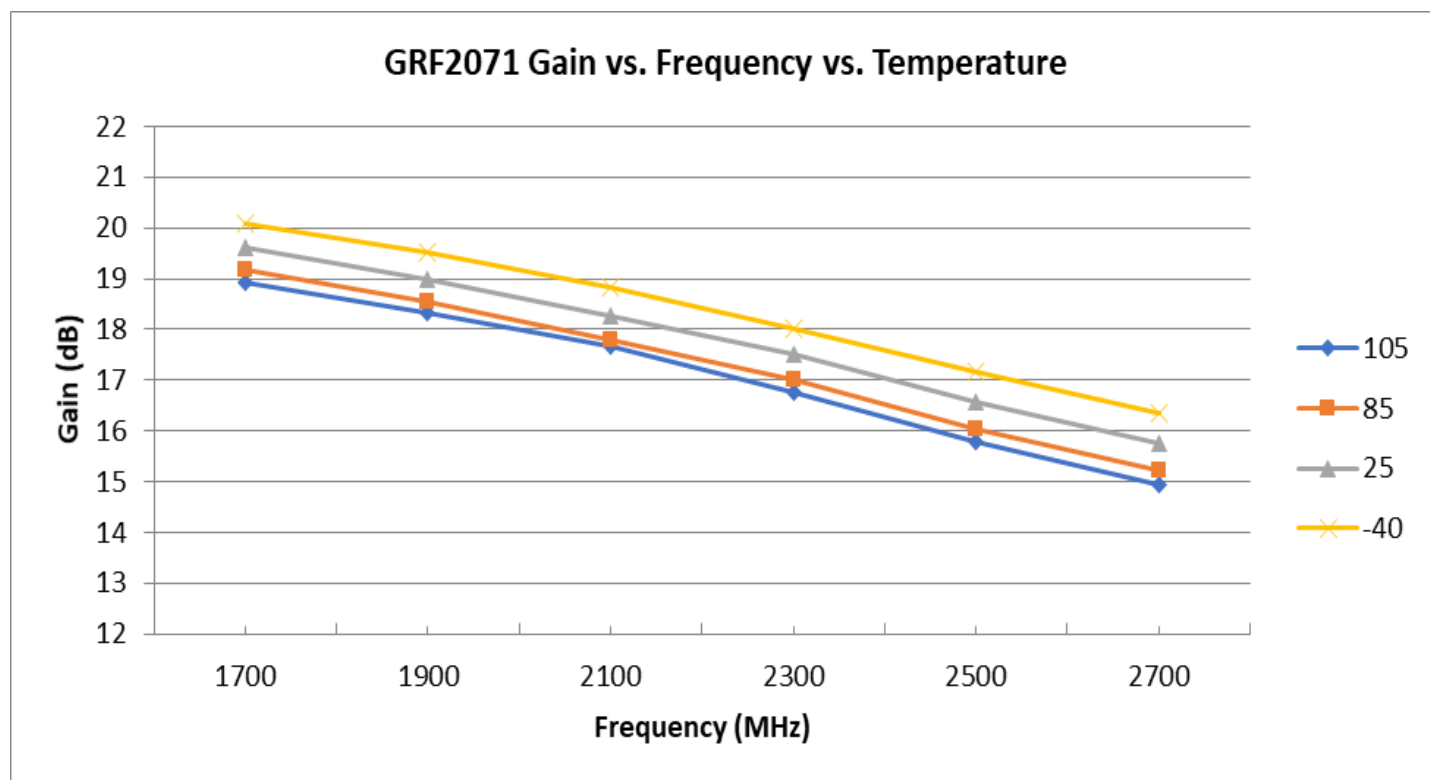
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Parameter	Symbol	Specification			Unit	Condition
		Min.	Typ.	Max.		
Gain	S21	18	19	21	dB	
Reverse Isolation	S12		-24		dB	F _{RF} : 1.7 to 2.7 GHz.
Evaluation Board Noise Figure	NF		0.36	0.56	dB	Evaluation Board SMA to SMA.
Output 3 rd Order Intercept Point	OIP3		36		dBm	+4 dBm P _{OUT} per tone at 2 MHz spacing (1899 and 1901 MHz).
Output 1 dB Compression Power	OP1dB	18.5	20		dBm	

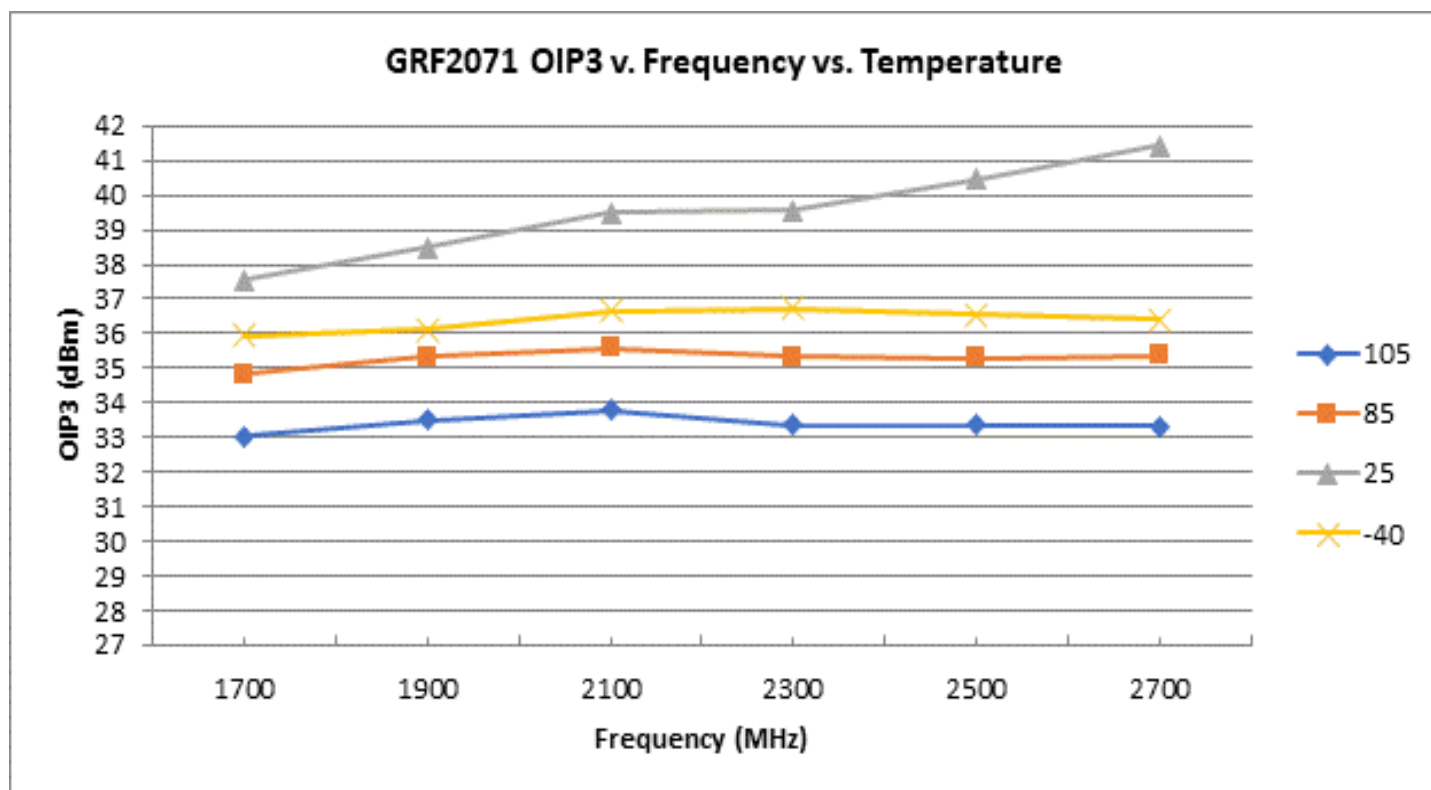
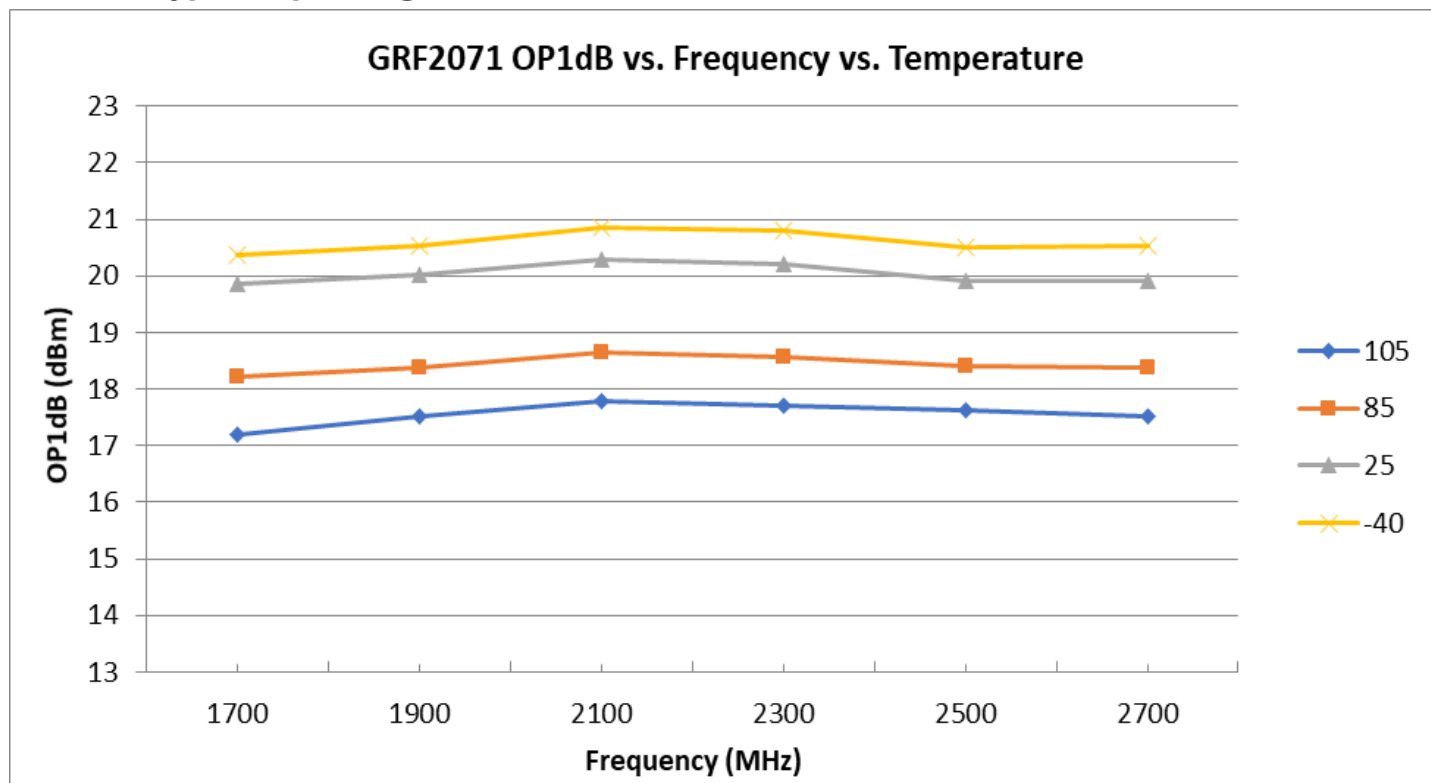
Typical Operating Curve Conditions

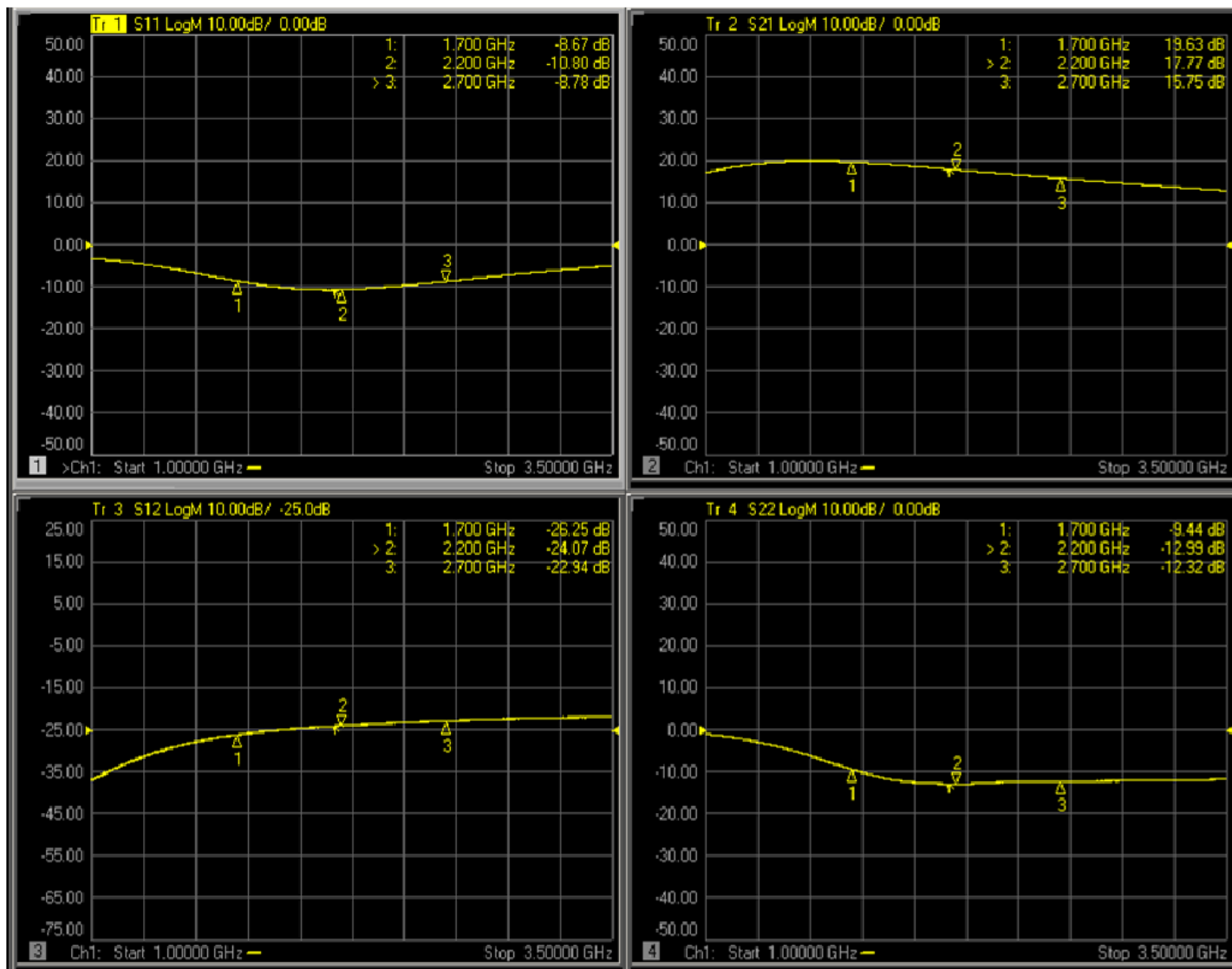
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GRF2071 Typical Operating Curves

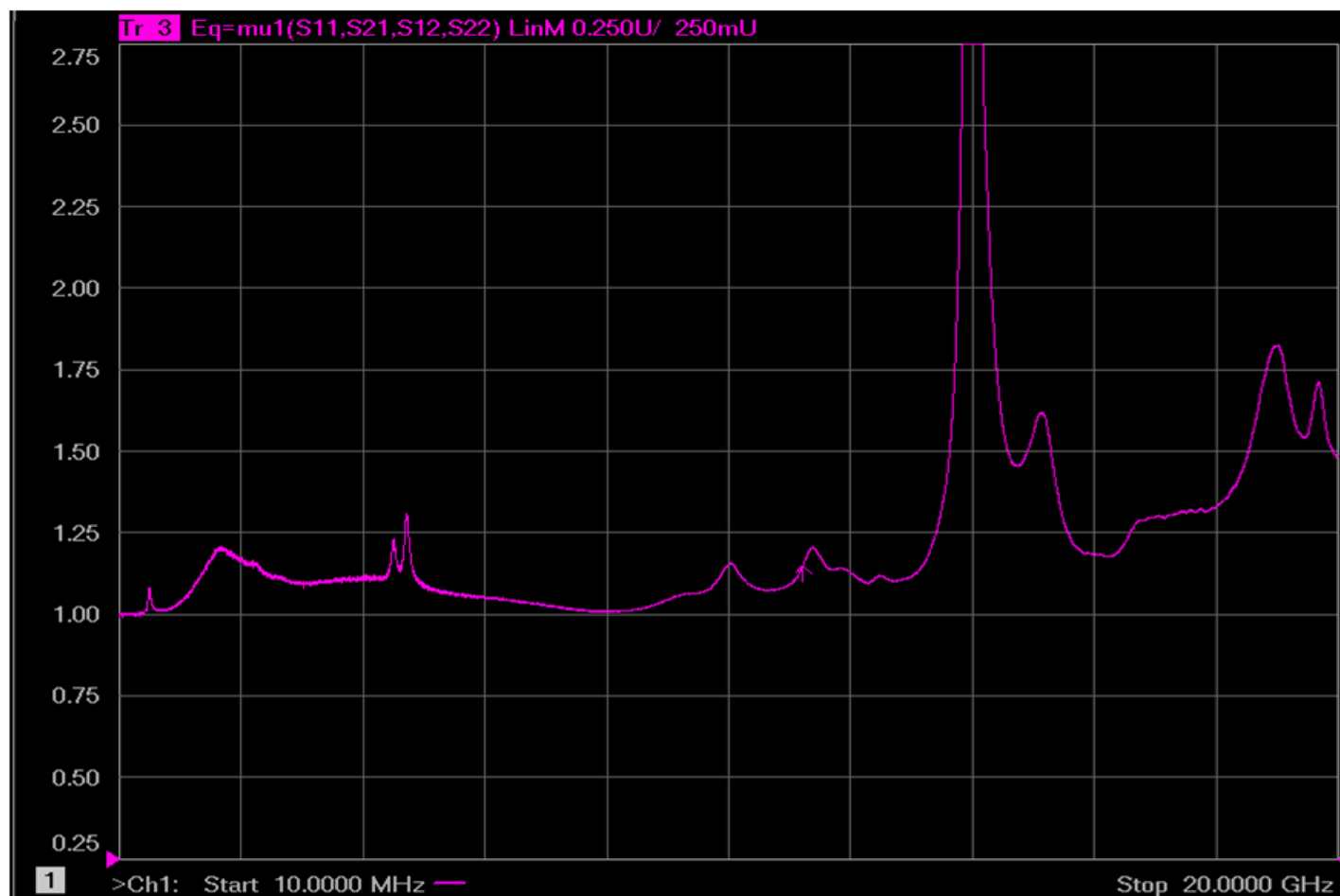


GRF2071 Typical Operating Curves

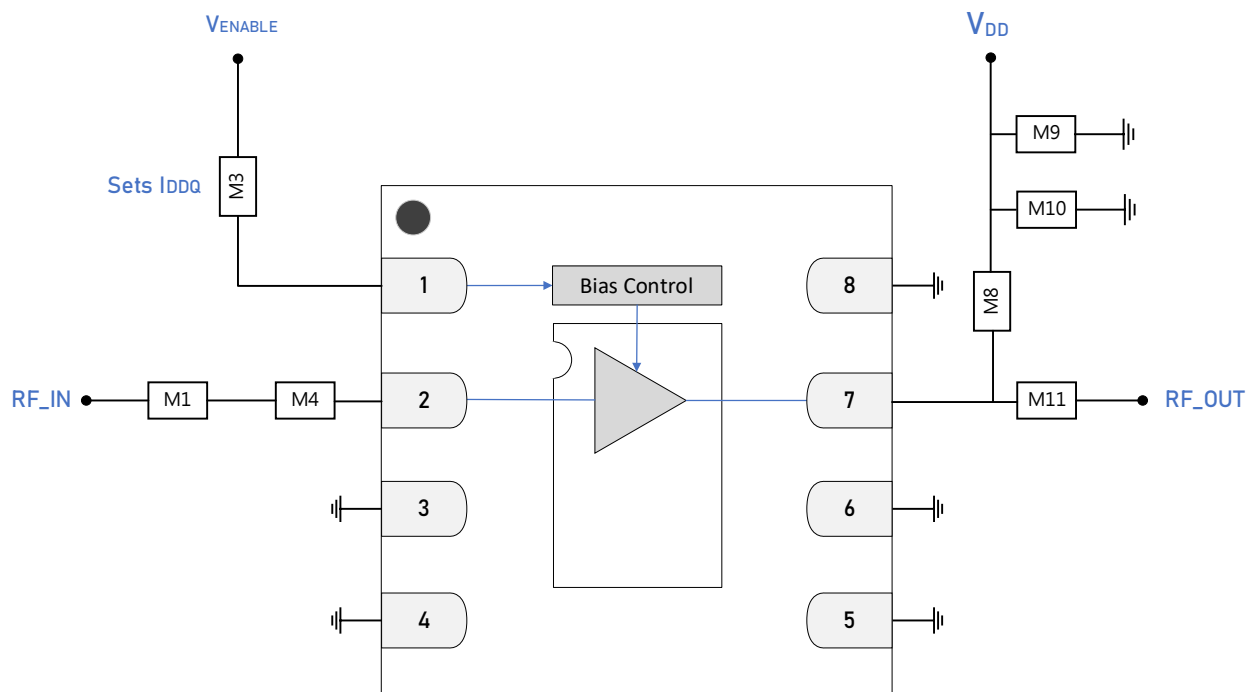


GRF2071 Typical Operating Curves: S-Parameters (1.7 to 2.7 GHz Tune)


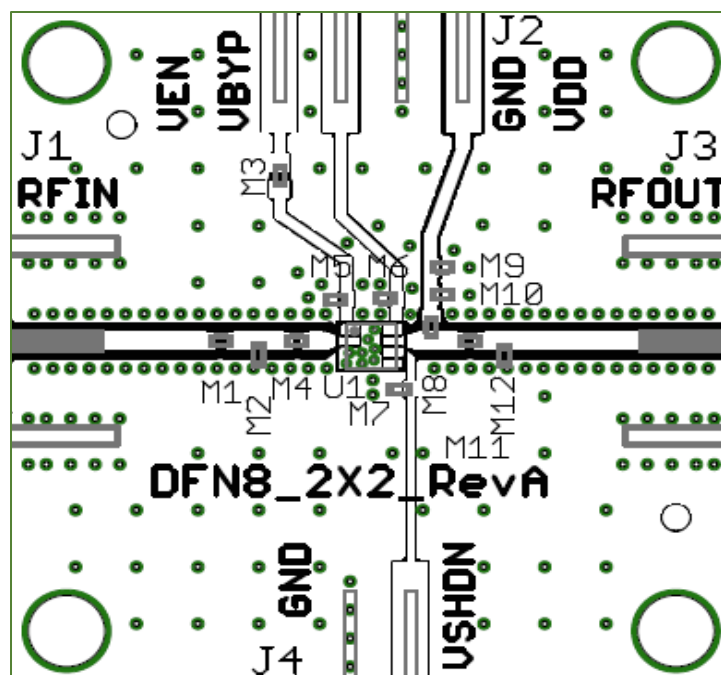
GRF2071 Typical Operating Curves: Stability Mu (1.7 to 2.7 GHz Tune)



Note: $\text{Mu} \geq 1.0$ implies unconditional stability.



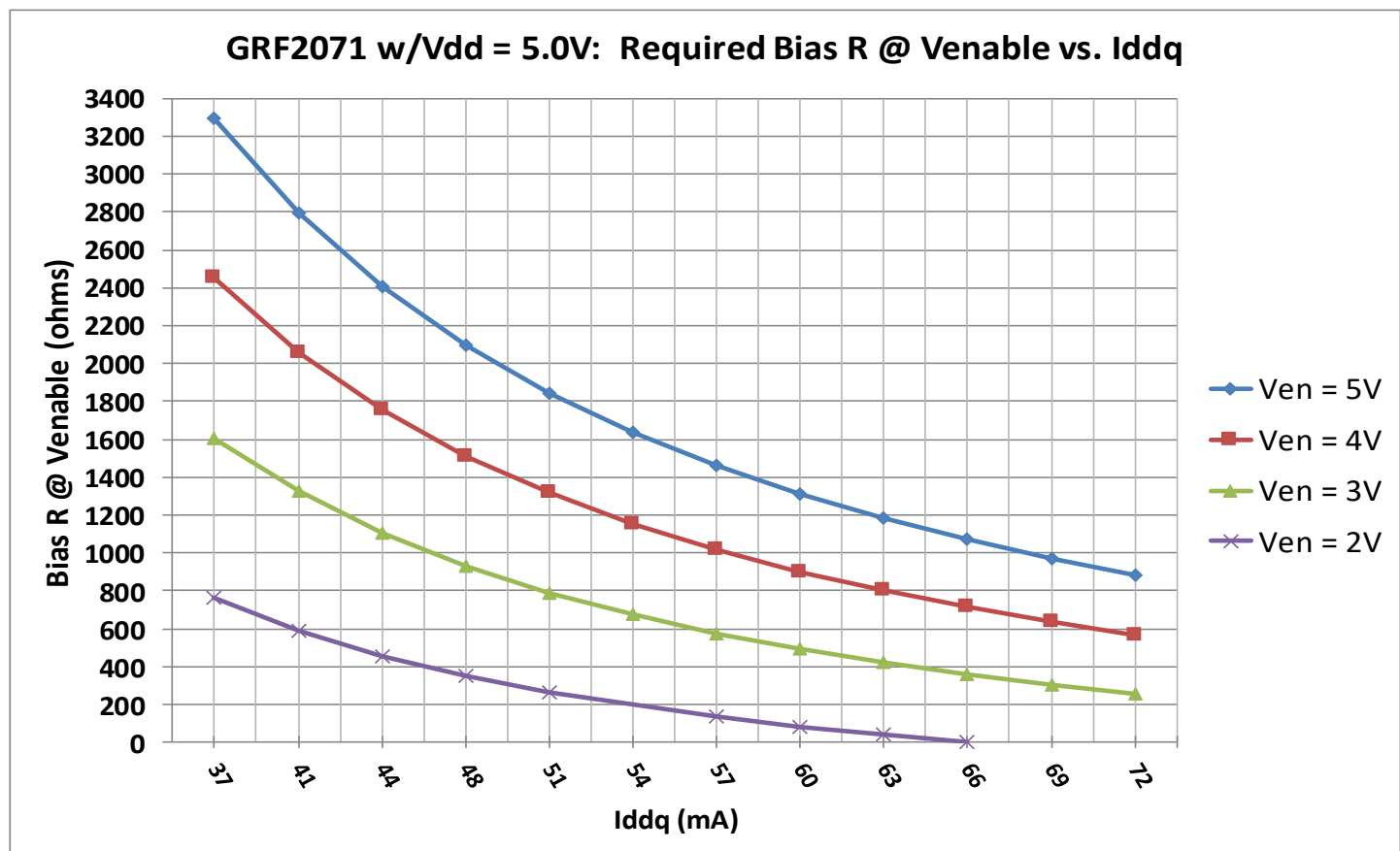
GRF2071 Standard Test Schematic

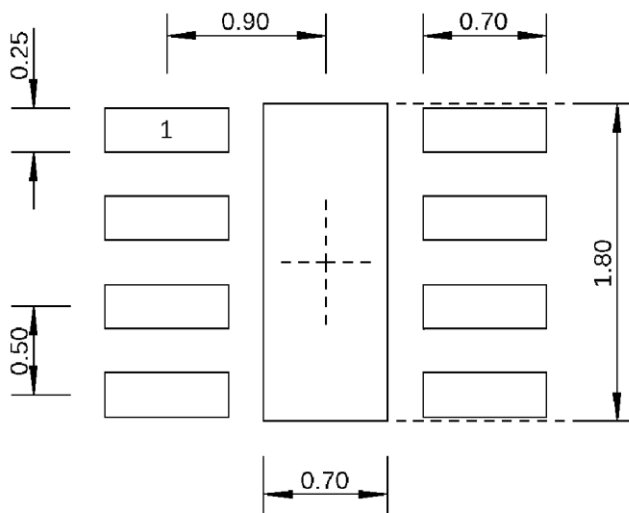


GRF2071 Evaluation Board Assembly Diagram

GRF2071 Evaluation Board Assembly Diagram Reference: 1.7 to 2.7 GHz Tune

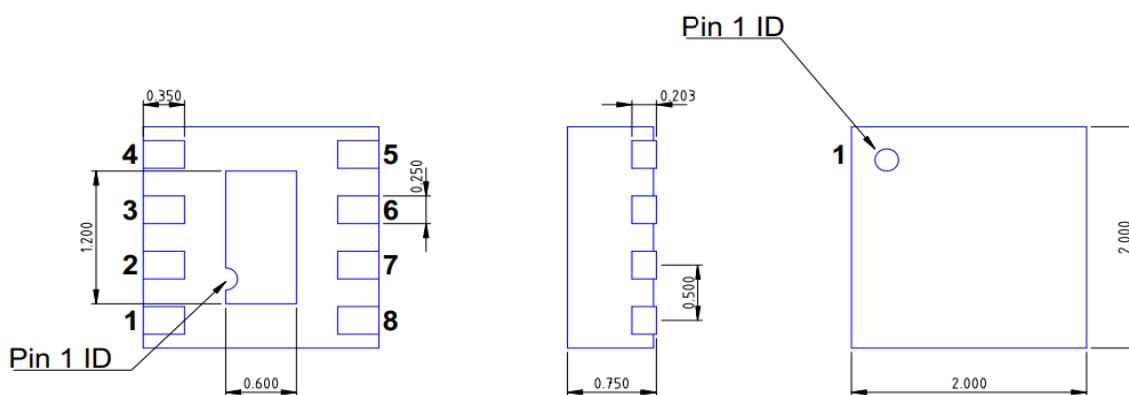
Component	Type	Manufacturer	Family	Value	Package Size	Substitution
M1	Capacitor	Murata	GJM	12 pF	0402	ok
M3 (sets I_{DDQ})	Resistor	Various	5%	See Curves	0402	ok
M4	Inductor	Coilcraft	HP	2.0 nH	0402	ok
M8	Inductor	Murata	LQG	3.3 nH	0402	ok
M9	Capacitor	Murata	GRM	0.1 μ F	0402	ok
M10	Capacitor	Murata	GRM	100 pF	0402	ok
M11	Capacitor	Murata	GJM	2.7 pF	0402	ok
Evaluation Board	DFN8_2x2_RevA					

GRF2071 Bias Resistor Selection Curves




Dimensions in millimeters

2.0 x 2.0 mm DFN-8 Suggested PCB Footprint (Top View)



Bottom View

Side View

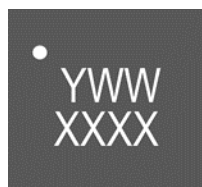
Top View

DFN8_2X2MM

Dimensions in millimeters
Dimensional Tolerance: ± 0.05

2.0 x 2.0 mm DFN-8 Package Dimensions

Package Marking Diagram



Line 1: "Y" = YEAR (single digit). "WW" = WORK WEEK the device was assembled.

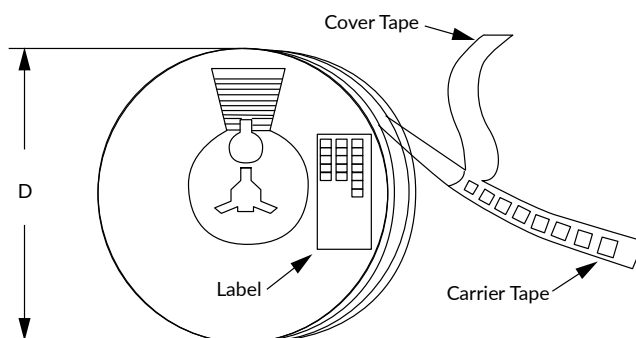
Line 2: "XXXX" = Device PART NUMBER.

Tape and Reel Information

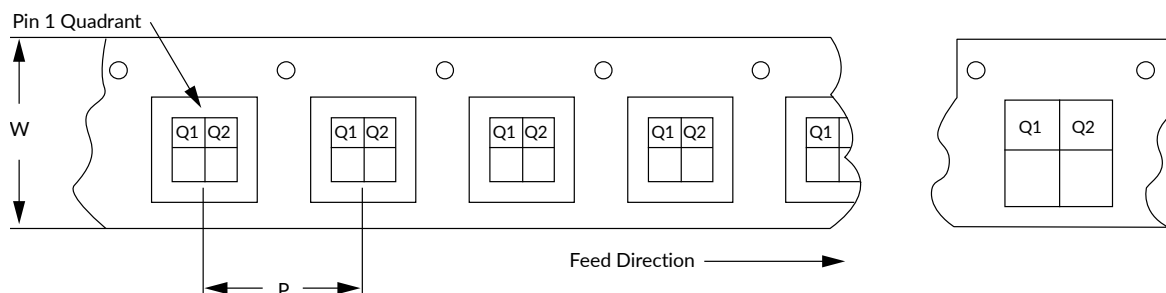
Guerrilla RF's tape and reel specification complies with Electronics Industries Association (EIA) standards for "Embossed Carrier Tape of Surface Mount Components for Automatic Handling" (reference EIA-481). See the following page for the Tape and Reel Specification and Device Package Information table, which includes units per reel.

Devices are loaded with pins down into the carrier pocket with protective cover tape and reeled onto a plastic reel. Each reel is packaged in a cardboard box. There are product labels on the reel, the protective ESD bag and the outside surface of the box.

For the Tape and Reel Reference Table, please refer to: [Package Manufacturing Information | Guerrilla RF \(guerrilla-rf.com\)](#)



Tape and Reel Packaging with Reel Diameter Noted (D)



Carrier Tape Width (W), Pitch (P), Feed Direction and Pin 1 Quadrant Information



Revision History

Revision Date	Description of Change
September 7, 2021	Converted format to new template.
May 31, 2023	Release B Data Sheet. Changed the following to match Production Test Plan: Added I_{DD} Min/Max limits. Raised I_{DD} typical from 60 to 70 mA. Lowered M3 from 1300 Ω to 1070 Ω .

Data Sheet Classifications

Data Sheet Status	Notes
Advance	S-Parameter and NF data based on EM simulations for the fully packaged device using foundry-supplied transistor S-Parameters. Linearity estimates based on device size, bias condition and experience with related devices.
Preliminary	All data based on limited evaluation board measurements taken within the Guerrilla RF Applications Lab. All parametric values are subject to change pending the collection of additional data.
Release Ø	All data based on measurements taken with <i>production-released</i> material. TYP values are based on a combination of ATE and bench-level measurements, with MIN/MAX limits defined using <i>modelled estimates</i> that account for part-to-part variations and expected process spreads. Although unlikely, future refinements to the TYP/MIN/MAX values may be in order as multiple lots are processed through the factory.
Release A-Z	All data based on measurements taken with production-released material <i>derived from multiple lots which have been fabricated over an extended period of time</i> . MIN/MAX limits may be refined over previous releases as more statistically significant data is collected to account for process spreads.

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